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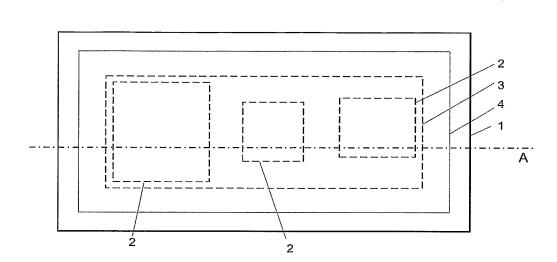
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(54) Title: ELECTRONIC DEVICE WITH STRESS RELIEF ELEMENT



(57) Abstract: The present invention relates to an electronic device whose component body contains a substrate and circuit elements placed on the substrate. An example embodiment contains at least one stress relief element (4), a substrate (1) with an upper surface and side walls at least one circuit element (2) located on said substrate (1) and at least one passivation layer (3) placed on said substrate (1), whereby said isolating layer (3) covers said at least one circuit element (2) and/or said substrate (1) and contains a top surface, at least one outer side surface which is located towards a side wall of said substrate and at least one outer edge, which is formed by said top surface and said at least one outer side surface, wherein the at least one stress relief element (4) is made out of a ductile material. On the passivation layer (3), the ductile material contacts the upper surface of the substrate (1).

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Declarations under Rule 4.17:

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ,

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